

# Schedule of CompSys 2017

Monday June 19, 2017

Time Slot	Speakers		Topic
9:00 9:20 9:40 10:00	Arrival		
10:20 10:40	Break		
11:00	Welcome		
11:20	Long	1	Wing Lung Ngai, Tim Hegeman, Stijn Heldens and Alexandru Iosup Granula: Toward Fine-grained Performance Analysis of Large-scale Graph Processing Platforms
11:40	Long	2	Merijn Verstraaten, Cees De Laat and Ana Lucia Varbanescu Level-optimized BFS: a Model-based Approach to Speed-up BFS Graph Traversal
12:00	Long	3	Bart van Strien, Harry Wijshoff and Kristian Rietveld Automatically Deriving Efficient Implementations of Parallel PageRank
12:20	Long	4	Ahmed MUSAafir and Ana Lucia Varbanescu Expanding graph datasets using sampling
12:40 13:00 13:20 13:40	Lunch		
14:00 14:20	Keynote		Andy Tanenbaum Lessons Learned from 30 Years of MINIX
14:40	Short	1	Mohammad Tahghighi, Henk Corporaal and Jos Huisken Brainwave: Low power EEG Signal Processing Platform
14:50	Short	2	Giulio Stramondo, Catalin Bogdan Ciobanu and Ana Lucia Varbanescu From Application Access Traces to Custom Parallel Memories
15:00	Short	3	Mark Wijtvliet, Luc Waeijen and Henk Corporaal Coarse Grained Reconfigurable Architectures in the Past 25 Years: Overview and Classification
15:10	Short	4	Julian Jansen, Ana-Maria Oprescu and Magiel Bruntink Measuring the impact of code quality analysis tools in Education
15:20	Short	5	Kristian Rietveld and Harry Wijshoff Providing A Compiler Technology-Based Alternative For Big Data Application Infrastructures
15:30	Break		
16:00	Long	5	Stephan van Schaik, Kaveh Razavi, Ben Gras, Herbert Bos and Cristiano Giuffrida RevAnC: A Framework for Reverse Engineering Hardware Page Table Caches
16:20	Long	6	C.B. Ciobanu, Giulio Stramondo, Ana Lucia Varbanescu and Cees De Laat A Feasibility Study for a PRF-based Parallel Memory on Maxeler's Dataflow Engine
16:40	Long	7	Shenjun Ma, Alexey S. Ilyushkin, Alexander Stegehuis and Alexandru Iosup ANANKE: a Q-Learning-Based Portfolio Scheduler for Complex Industrial Workflows
17:00	End of day		
17:15	Senior staff meeting		
18:30-21:00	Dinner		

# Schedule of CompSys 2017

Tuesday June 20, 2017

Time Slot	Speakers		Topic
9:00 9:20 9:40 9:50	Keynote		Henk Sips Academic career perspectives: a changing scene
10:00	Short	7	Pim Otte, Martijn de Vos, Johan Pouwelse TrustChain: A Sybil-resistant Scalable Blockchain
10:10	Short	8	Justyna Chromik, Anne Remke and Boudewijn Haverkort A Testbed for locally Monitoring SCADA Networks in Smart Grids
10:20	Short	9	Paco van Beckhoven, Ana-Maria Oprescu and Magiel Bruntink Predicting Test Suite Effectiveness Using Static Analysis
10:30	Break		
11:00	Long	8	Roshan Bharath Das, Nicolae Vladimir Bozdog and Henri Bal Cowbird: A Flexible Cloud-based Framework for Combining Smartphone Sensors and IoT
11:20	Long	9	Marc X. Makkes, Alexandru Uta, Roshan Bharath Das, Vladimir Bozdog and Henri Bal P2-SWAN: Real-time Privacy Preserving Computation for IoT Ecosystems
11:40	Long	10	Nicolae Vladimir Bozdog, Marc X. Makkes, Alexandru Uta, Roshan Bharath Das, Aart Van Halteren and Henri Bal SenseLE: Exploiting Spatial Locality in Decentralized Sensing Environments
12:00	Long	11	Alexandru Iosup, Tim Hegeman, Wing Lung Ngai, Stijn Heldens, Arnau Prat-Perez, Thomas Manhardt, Hassan Chafi, Mihai Capota, Narayanan Sundaram, Michael Anderson, Ilie Gabriel Tanase, Yinglong Xia, Lifeng Nai and Peter Boncz LDBC Graphalytics: A Benchmark for Large-Scale Graph Analysis on Parallel and Distributed Platforms
12:20	Long	12	Maria Voinea and Alexandru Iosup POSUM: A Portfolio Scheduler for MapReduce Workloads
12:40 13:00 13:20 13:40	Lunch		
14:00 14:20	Keynote		Manish Parashar Big Data Challenges in Extreme Scale Science
14:40	Short	10	Laurens Versluis, Erwin van Eyk and Alexandru Iosup An Analysis of Workflow Formalisms for Complex Workflows with Non-Functional Requirements
14:50	Short	11	Luc Waeijen, Savvas Sioutas, Barry de Bruin, Henk Corporaal and Maurice Peemen Fast exploration of code transformations in CNNs to reduce external memory accesses
15:00	Short	14	Vincent van Beek, Giorgos Oikonomou, Jesse Donkervliet, Stefan Hugtenburg, Siqi Shen and Alexandru Iosup Resource Management and Scheduling in Cloud Datacenters that Host Business-Critical Workloads
15:10	Short	13	Lucian Cojocar, Kaveh Razavi and Herbert Bos Off-the-shelf Embedded Devices as Platforms for Security Research
15:20	Short	14	Adrian Zborowski and Clemens Grelck Oxidize: Open Framework for Idiomatic Rule Preservation
15:30 16:00 16:20 16:40 17:00 17:15	Social Event		
18:30-21:00	Dinner		

# Schedule of CompSys 2017

Wednesday June 21, 2017

Time Slot	Speakers		Topic	
9:00	Keynote		Cyber-Physical Systems and Energy: A Sustainable Team	
9:20		Boudewijn Haverkort		
9:40				
10:00	Long	13	Alessio Sclocco, Henri Bal and Rob Van Nieuwpoort	Tuning Difficulty on Many-Core Accelerators
10:20	Short	12	Alexandru Iosup, Georgios Andreadis, Vincent van Beek, Matthijs Bijman, Erwin van Eyk, Mihai Neacsu, Leon Overweel, Satchendra Talluri, Laurens Versluis and Maaïke Visser	The OpenDC Vision: Towards Collaborative Datacenter Simulation and Exploration for Everybody
10:30	Break			
11:00	Long	14	Morteza Karimzadeh, Luca Valtulina, Aiko Pras, Hans van den Berg, Ricardo de O. Schmidt, Marco Liebsch and Tarik Taleb	Double-NAT Based Mobility Management for Future LTE Networks
11:20	Long	15	Jesse Donkervliet, Jerom van der Sar and Alexandru Iosup	Opencraft: Towards Massivizing Modifiable Virtual Environments
11:40	Long	16	Anton Wijs, Thomas Neele and Dragan Bosnacki	GPUexplore 2.0: Unleashing GPU Explicit-State Model Checking
12:00	Long	17	Stijn Heldens, Ana Lucia Varbanescu and Alexandru Iosup	High-Performance Graph Processing on Heterogeneous CPU-GPU Platforms
12:20	Lunch			
12:40				
13:00				
13:20	Closing, best presentation awards, announcement for next year.			